

IN THE CLAIMS:

The text of all pending claims, (including withdrawn claims) is set forth below. Cancelled and not entered claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with ~~striketrough~~. The status of each claim is indicated with one of (original), (currently amended), (cancelled), (withdrawn), (new), (previously presented), or (not entered).

Please AMEND claim 2 in accordance with the following:

1. (original) A differential amplifier, comprising:
two amplifier transistors arranged in parallel paths and having emitters, respectively;
a compensation resistor connecting the emitters of said two amplifier transistors and
having a negative temperature coefficient;
a common emitter line connected to the emitters of said two amplifier transistors; and
a current source, connected to said common emitter line, to produce a quiescent current independent of temperature.
2. (currently amended) The differential amplifier as claimed in claim 1, wherein said compensation resistor is a series circuit formed by a negative temperature coefficient resistor and a temperature independent series resistor.
3. (original) The differential amplifier as claimed in claim 2, wherein said current source is connected to a reference voltage and includes
a bias resistor;
a bias transistor in series with said bias resistor and having a base and an emitter; and
an operational amplifier having an output connected to the base of said bias transistor, a first input connected to the emitter of said bias transistor, and a second input connected to the reference voltage with respect to said current to be applied.
4. (original) The differential amplifier as claimed in claim 3, wherein a bandgap norm is used for the reference voltage.